



DUAL DIFFERENTIAL COMPARATOR

■ DESCRIPTION

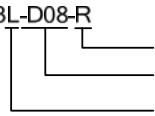
The UTC LM393 consists of two independent voltage comparators, designed specifically to operate from a single power supply over a wide voltage range.

■ FEATURES

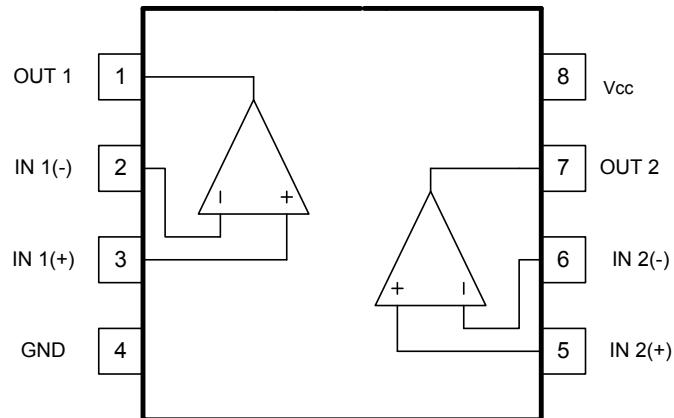
- * Single or dual supply operation.
- * Wide operating supply range ($V_{CC}=2V \sim 36V$ or $\pm 1 \sim \pm 18V$)
- * Input common-mode voltage includes ground.
- * Low supply current drain $I_{CC}=0.8mA$ (Typical).
- * Low input bias current $I_{BIAS}=25nA$ (Typical).
- * Output compatible with TTL, DTL, and CMOS logic system.

■ ORDERING INFORMATION

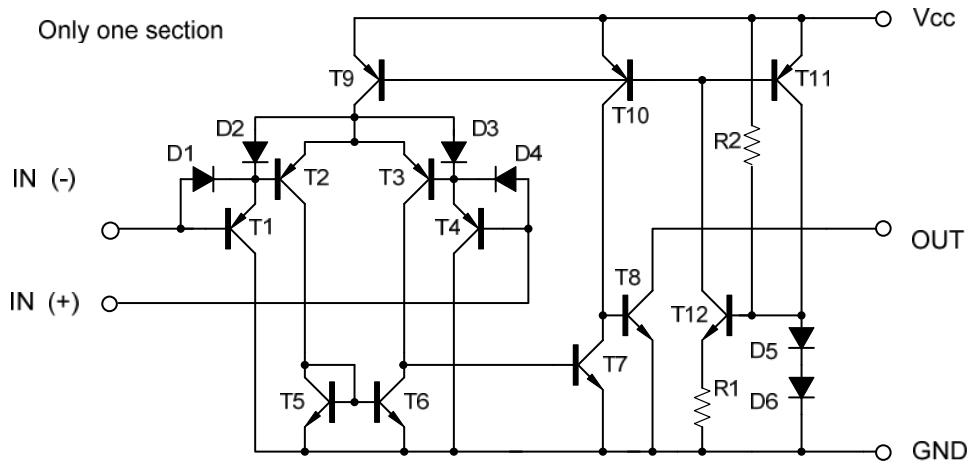
Ordering Number		Package	Packing
Lead Free Plating	Halogen-Free		
LM393L-D08-T	LM393G-D08-T	DIP-8	Tube
LM393L-P08-R	LM393G-P08-R	TSSOP-8	Tape Reel
LM393L-S08-R	LM393G-S08-R	SOP-8	Tape Reel

	<p>(1)Packing Type (2)Package Type (3)Lead Plating</p> <p>(1) R: Tape Reel, T: Tube (2) D08: DIP-8, P08: TSSOP-8, S08: SOP-8 (3) G: Halogen Free, L: Lead Free Plating Blank: Pb/Sn</p>
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■ PIN DESCRIPTION



■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V _{CC}	±18 or 36	V
Differential Input Voltage		V _{I(DIFF)}	±36	V
Input Voltage		V _{IN}	-0.3 ~ +36	V
Power Dissipation	DIP-8	P _D	600	mW
	SOP-8		420	mW
	TSSOP-8		350	mW
Operating Temperature Range		T _{OPR}	-20 ~ +85	°C
Storage Temperature Range		T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

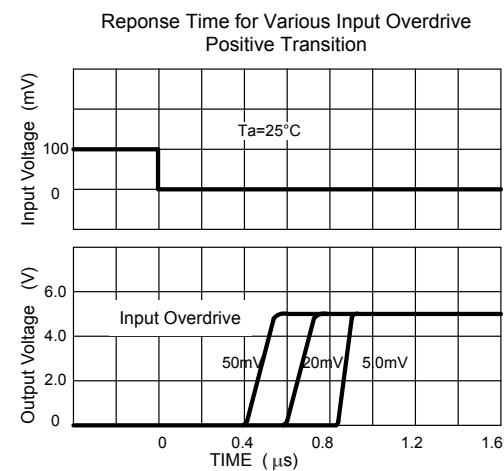
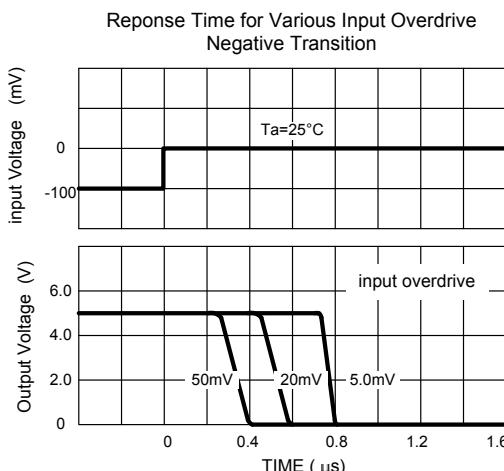
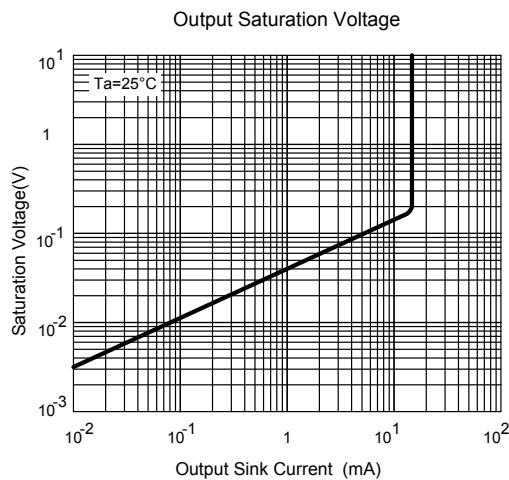
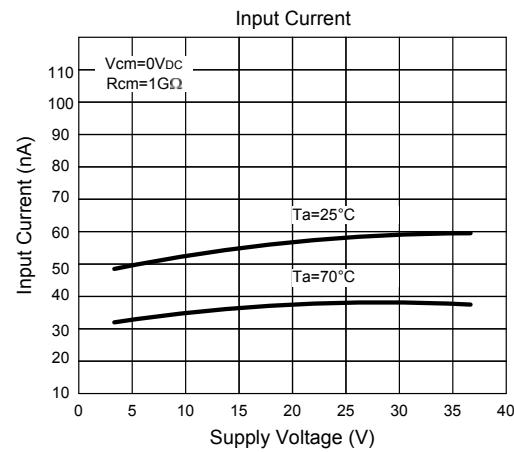
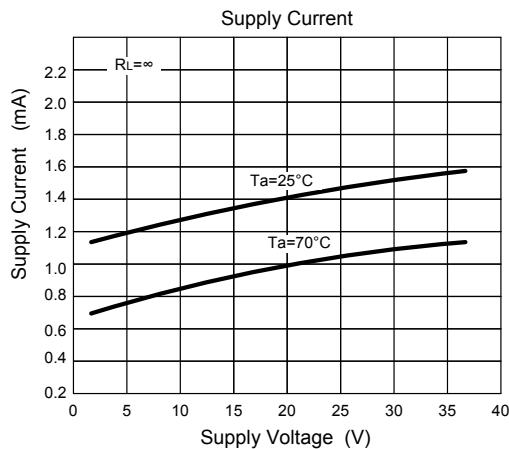
Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS

(V_{CC}=5.0V, Ta=25°C, All voltage referenced to GND unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{I(OFF)}	V _{CM} =0V to V _{CC} -1.5V V _{O(P)} =1.4V, R _S =0Ω			1.0	5.0	mV
Output Saturation Voltage	V _{SAT}	V _{I(-)>1V} , V _{I(+)=0V} , I _{SINK} =4mA			160	400	mV
Input Common Mode Voltage	V _{I(CM)}	V _{CC} =30V		0		V _{CC} -1.5	V
Large Signal Voltage Gain	G _V	V _{CC} =15V, R _L ≥15KΩ		50	200		V/mV
Power Supply Current	I _{CC}	R _L =∞, V _{CC} =30V			0.8	2.5	mA
		R _L =∞			0.6	1.0	mA
Input Offset Current	I _{I(OFF)}				5	50	nA
Input Bias Current	I _{I(BIAS)}				65	250	nA
Output Sink Current	I _{O(SINK)}	V _{I(-)>1V} , V _{I(+)=0V} , V _{O(p)<1.5V}		6	18		mA
Output Leakage Current	I _{O(LEAK)}	V _{I(+)=1V} , V _{I(-)=0}	V _{O(p)= 5V}		0.1		nA
			V _{O(p)=30V}			1.0	μA
Large Signal Response Time	t _R	V _{IN} =TTL logic swing V _{REF} =1.4V, V _{RL} =5V, R _L =5.1kΩ			350		ns
Response Time	t _R	V _{RL} =5V, R _L =5.1kΩ			1400		ns

■ TYPICAL CHARACTERISTICS



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